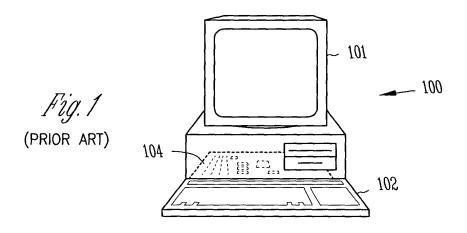


TITLE: DEVICE AND METHOD TO REDUCE WORDLINE RC CONSTANT IN SEMICONDUCTOR MEMORY DEVICES

INVENTORS NAME: Huy Thanh Vo SERIAL NO.: 09/808,750

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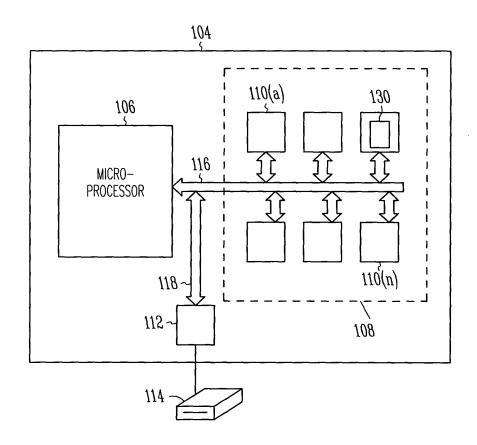


Fig. 2 (PRIOR ART)

TITLE: DEVI

AND METHOD TO REDUCE WORDLINE RC IN SEMICONDUCTOR MEMORY DEVICES INVENTORS NAME: Huy Thanh Vo

SERIAL NO.: 09/808,750

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CONSTANT

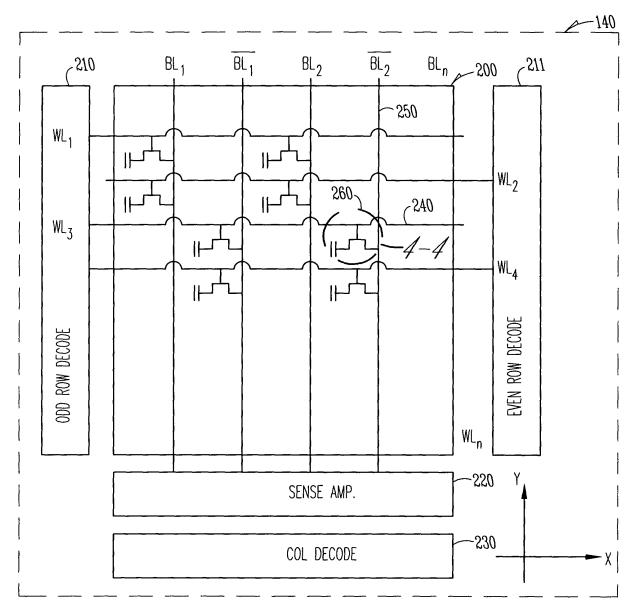


Fig. 3 (PRIOR ART)



TITLE: DEVICAND METHOD TO REDUCE WORDLINE RC IN SEMICONDUCTOR MEMORY DEVICES INVENTORS NAME: Huy Thanh Vo SERIAL NO.: 09/808,750

260

270

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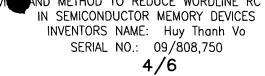
Fig. 4

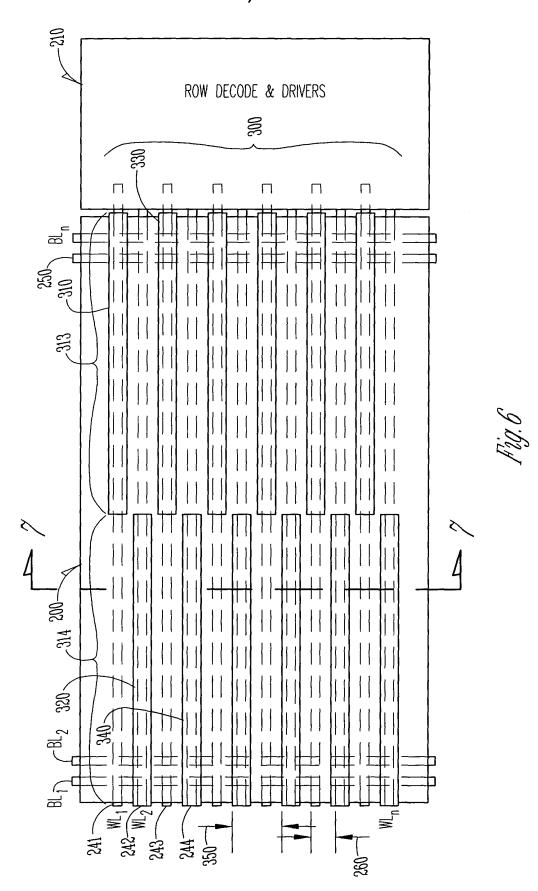
(PRIOR ART)

140 314 313 210 211 310 311--500 315 WL_1 320 241 322 WL_2 242 WL_3 243 WL_4 244-DECODE EVEN ROW DECODE ROW 00 -550 SENSE AMP. 530 COL DECODE

TITLE: DEVI AND METHOD TO REDUCE WORDLINE RC IN SEMICONDUCTOR MEMORY DEVICES

CONSTANT







TITLE: DEV AND METHOD TO REDUCE WORDLINE RC IN SEMICONDUCTOR MEMORY DEVICES
INVENTORS NAME: Huy Thanh Vo
SERIAL NO.: 09/808,750

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